

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1-2. (canceled)

3. (currently amended): A system suitable for providing integrated circuit design, comprising:

a memory storing a first set of instructions and a second set of instructions;

and

a processor communicatively coupled to the memory, the processor suitable for performing the first set of instructions and the second set of instructions, wherein the first set of instructions is suitable for configuring a processor to provide an integrated circuit development environment in which a support methodology for an integrated circuit is created and the second set of instructions is suitable for configuring a processor to provide tools for implementing a platform architecture of an integrated circuit in which the platform architecture supplies a structure of the integrated circuit, the first set of instructions and the second set of instructions linked through at least one formalism so that at least one of an action taken utilizing the platform architecture influences the support methodology and an action taken utilizing the support methodology influences the platform architecture,

wherein the formalism includes a differential game, and

The system as described in claim 2, wherein the differential game is a zero sum game.

4-11. (canceled)

12. (currently amended): A method of designing an integrated circuit, comprising: